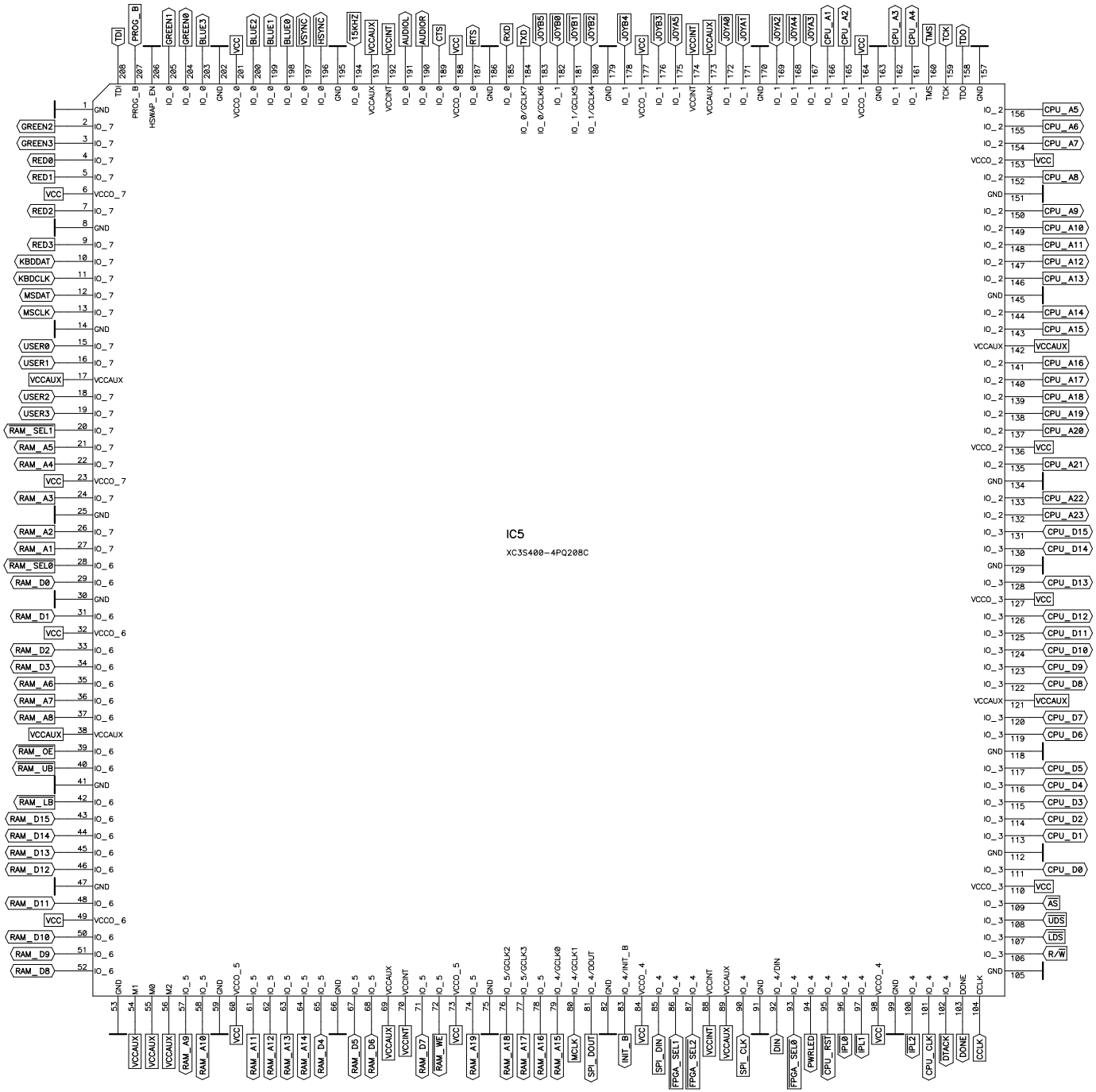
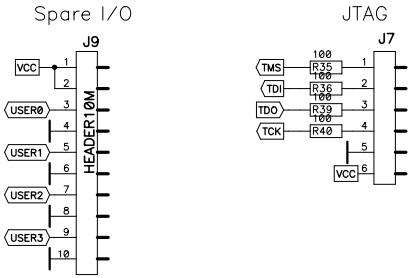
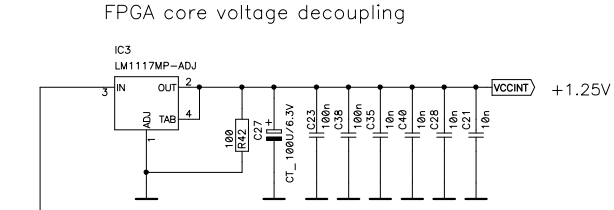
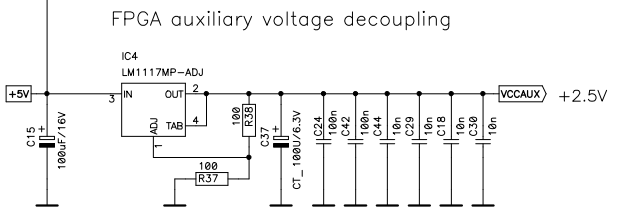
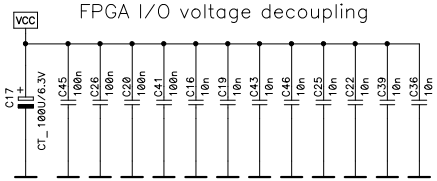


PATCH needed to get rev 1 board working:
Disconnect net SPI_DOUT from pin 81 of FPGA.
Connect net SPI_DOUT to pin 19 of FPGA (net USER3).
REASON:
Pin 81 is an output during FPGA config that blocks SPI to MMC during startup.

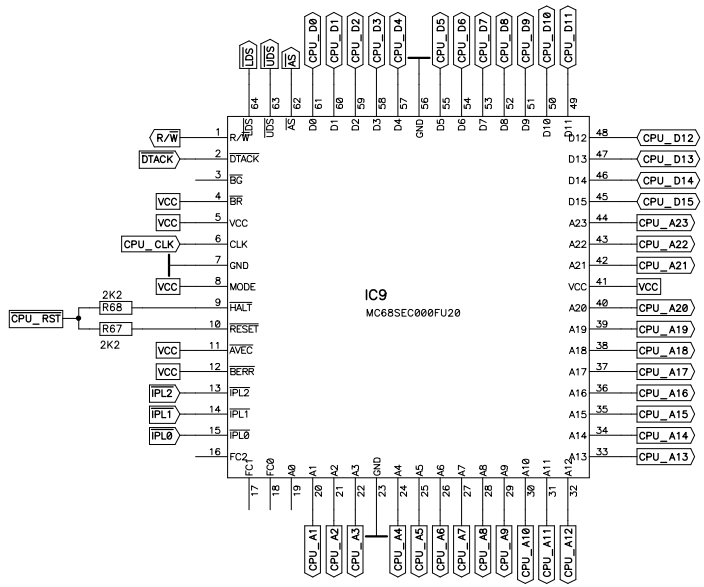


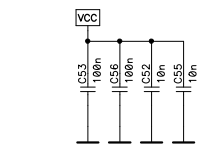


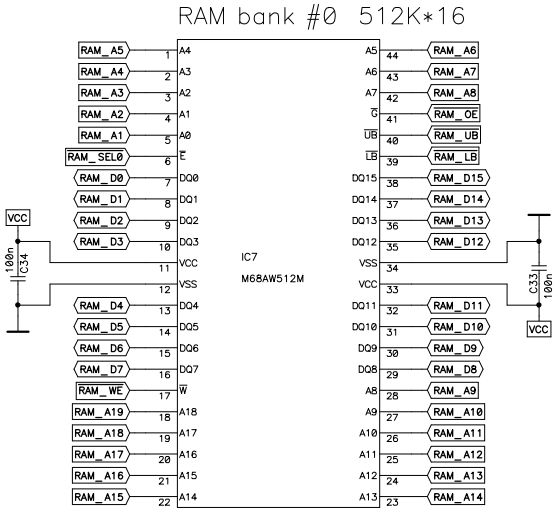


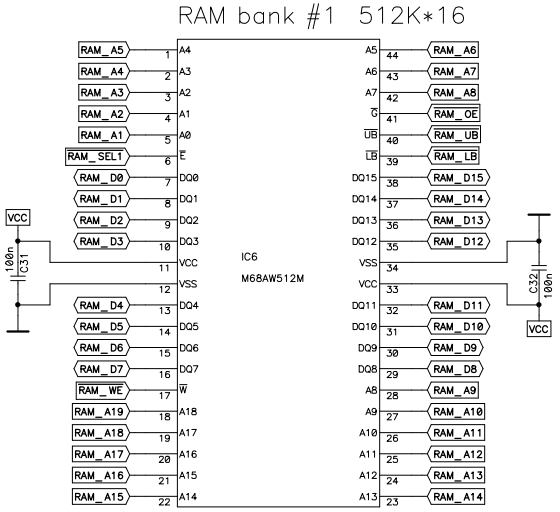


Title	MINIMIG REV 1.0	
Size A2	Number SPARTAN POWER	Rev 2
Date 03-10-2007		Drawn by Dennis van Weeren
Filename Minimig1		Sheet 1 of 3

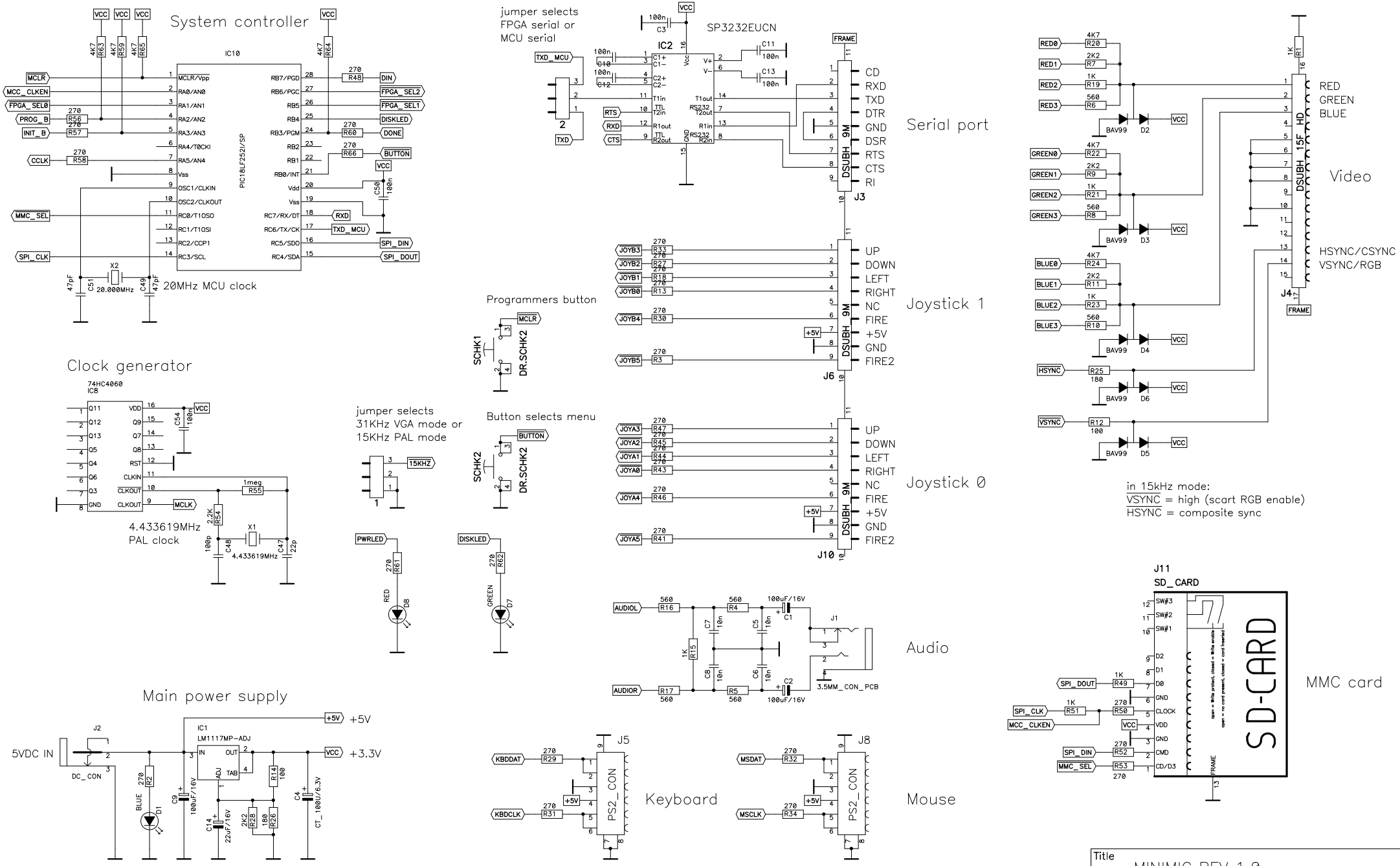








Title	MINIMIG REV 1.0	
Size A2	Number 68000 FOREVER & RAM	Rev 2
Date 03-10-2007		Drawn by Dennis van Weeren
Filename Minimig1		Sheet 2 of 3



Title	MINIMIG REV 1.0	
Size A2	Number I/O GALORE	Rev 2
Date 03-10-2007	Drawn by Dennis van Weeren	
Filename Minimig1	Sheet 3 of 3	