Top assembly

Component placement, fitted-side orientation and 3D reference

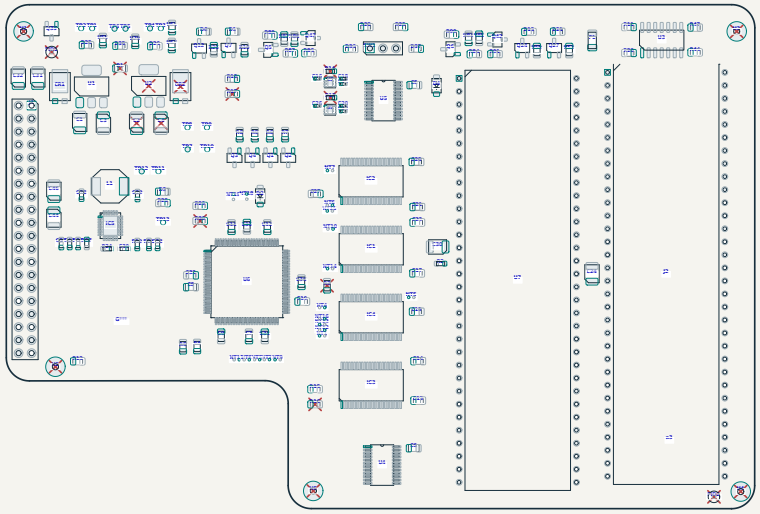
ASSEMBLY NOTES

01 View from outside the top of the board. Use connector edges to orient the board.

02 Teal identifies pad 1 / A1. Red crosses mark DNP footprints. Confirm polarity from the footprint and datasheet.

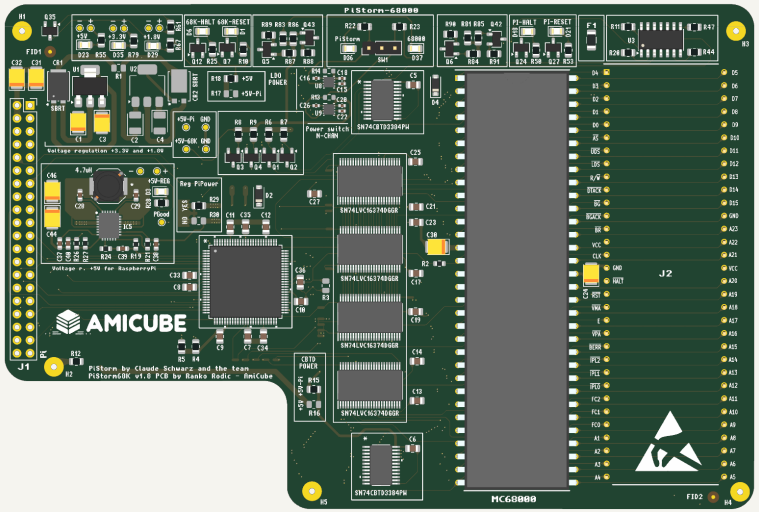
03 The component table controls values and population. The 3D view is illustrative; use the scale below for the plan.

REFERENCE PLAN



25 mm scale check / plan 0.68:1

3D ASSEMBLY VIEW



145.93 × 98.25 mm outline

ASSEMBLY INVENTORY

Placement	Footprints	SMD	DNP
Top side	176	134	18
Bottom side	4	2	2

Saved pads and fabrication outlines form the reference plan. Component values, connector pin maps and source notes are in documentation/index.html.

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1.8 • generated 2026-10-16 • source 82030dcfa626

Units: mm. Source CAD governs. Regenerate after design changes.

Documentation only; no electrical symbols

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Sheet: /Board guide and engineering reference/Top assembly/
File: AssemblyTop.kicad_sch

Title: AmiCube PiStorm68K / Top assembly

Size: A4	Date: 2026-10-16	**Rev: 1.8**
KiCad E.D.A. 10.0.7		Id: 12/16

Bottom assembly

Component placement, fitted-side orientation and 3D reference

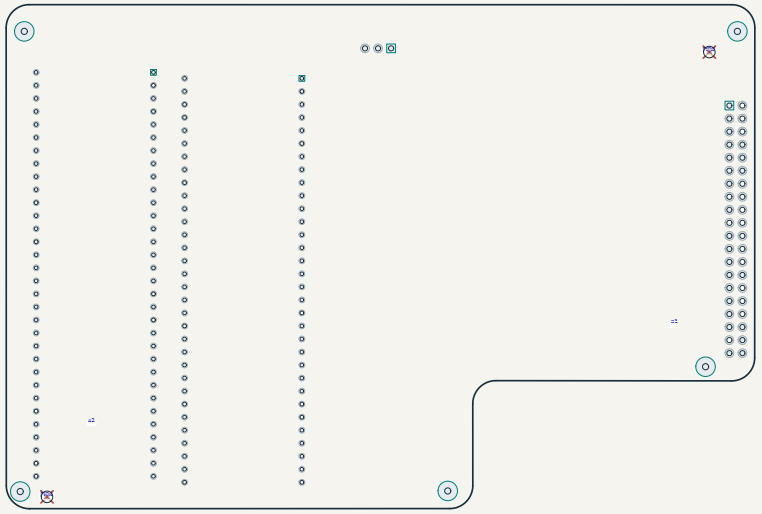
ASSEMBLY NOTES

01 View from outside the bottom of the board. Bottom plan is mirrored.

02 Teal identifies pad 1 / A1. Red crosses mark DNP footprints. Confirm polarity from the footprint and datasheet.

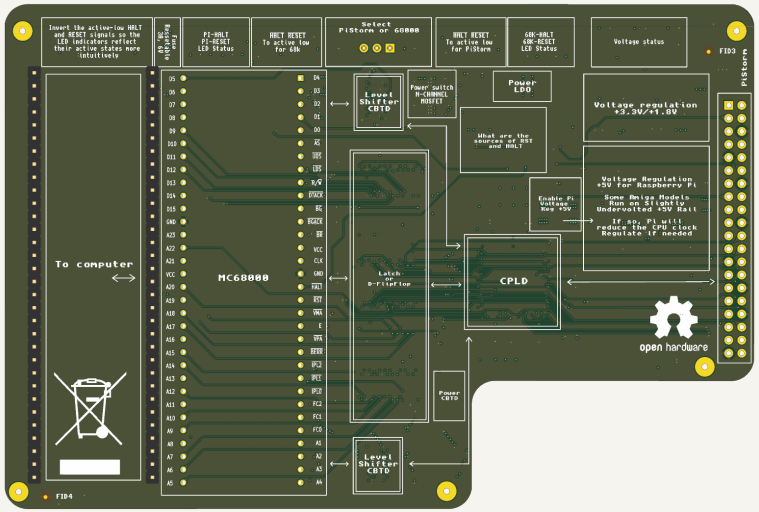
03 The component table controls values and population. The 3D view is illustrative; use the scale below for the plan.

REFERENCE PLAN



25 mm scale check / plan 0.68:1

3D ASSEMBLY VIEW



145.93 × 98.25 mm outline

ASSEMBLY INVENTORY

Placement	Footprints	SMD	DNP
Top side	176	134	18
Bottom side	4	2	2

Saved pads and fabrication outlines form the reference plan. Component values, connector pin maps and source notes are in documentation/index.html.

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Sheet: /Board guide and engineering reference/Bottom assembly/
File: AssemblyBottom.kicad_sch

Title: AmiCube PiStorm68K / Bottom assembly

Size: A4	Date: 2026-10-16	Rev: 1.8
KiCad E.D.A. 10.0.7		Id: 13/16

Layer stackup and fabrication

Source project settings. Dimensions and fabrication parameters come from the saved PCB.

Layer	Type	Material	mm
F.SilkS	Top Silk Screen	—	—
F.Paste	Top Solder Paste	—	—
F.Mask	Top Solder Mask	—	0.01
F.Cu	copper	—	0.035
dielectric 1	prepreg	FR4	0.1
In1.Cu	copper	—	0.035
dielectric 2	core	FR4	0.535
In2.Cu	copper	—	0.035
dielectric 3	prepreg	FR4	0.1
In3.Cu	copper	—	0.035
dielectric 4	core	FR4	0.535
In4.Cu	copper	—	0.035
dielectric 5	prepreg	FR4	0.1
B.Cu	copper	—	0.035
B.Mask	Bottom Solder Mask	—	0.01
B.Paste	Bottom Solder Paste	—	—
B.SilkS	Bottom Silk Screen	—	—

Saved board parameters

Property	Saved value
Outline	145.93 × 98.25 mm
Board thickness	1.6 mm
Copper layers	6
Footprints	180
Track/arc items	3875
Vias	782

Saved routing rules / mm

Class	Clearance	Track	Via/drill
Default	0.15	0.1	0.3/0.15
Data	0.15	0.12	0.3/0.15
Power	0.15	0.3	0.3/0.15

These drawings record the source design. Confirm material, layer stack and fabrication capabilities against the native project before manufacture.

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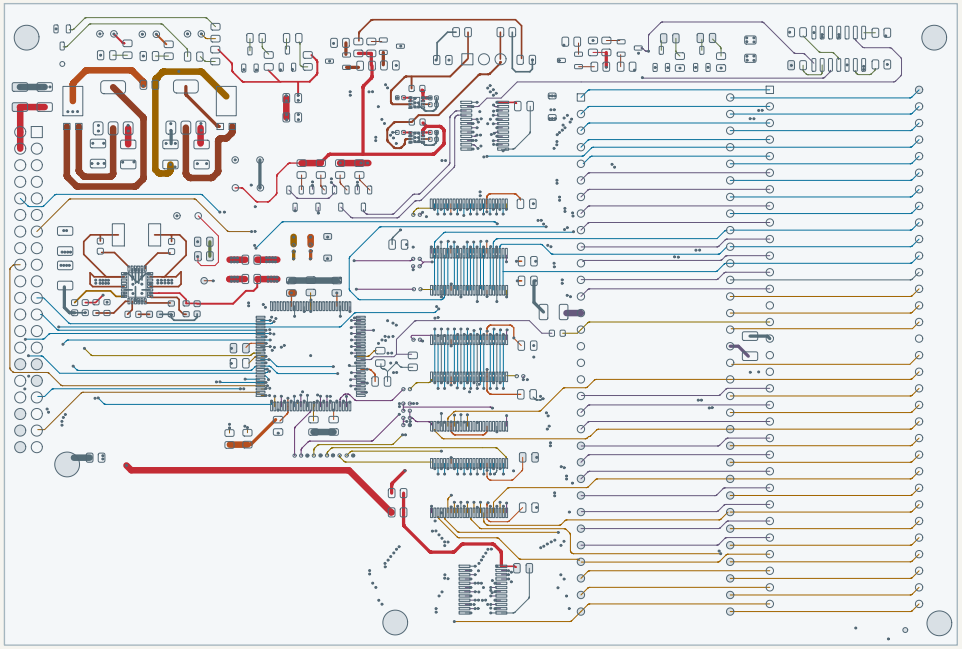
Units: mm. Source CAD governs. Regenerate after design changes.

Documentation only; no electrical symbols
Minimig.ca / Minimig
Sheet: /Board guide and engineering reference/Layer stackup and fabrication/
File: Stackup.kicad_sch
Title: AmiCube PiStorm68K / Layer stackup and fabrication
Size: A4 | Date: 2026-10-16 | Rev: 1.8
KiCad E.D.A. 10.0.7 | Id: 14/16

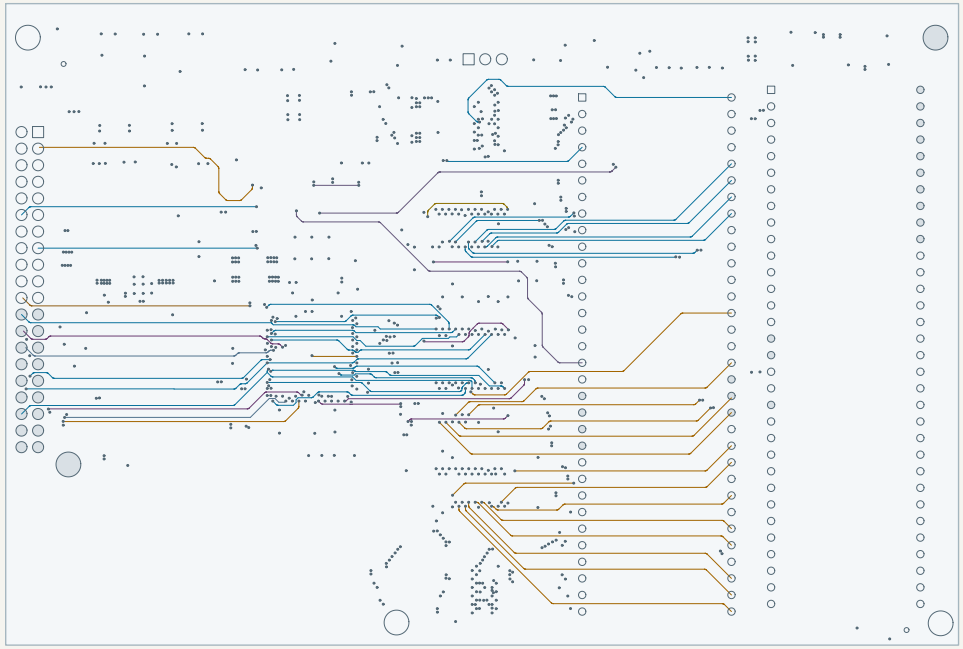
Routing guide

Outer copper from saved tracks, pads and vias. Filled zones are omitted for clarity.

Front copper



Back copper / through-board view



Colours identify net function. Grey pads and vias retain the saved geometry. Use native PCB layers for inner copper, zone fills and fabrication output.

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Units: mm. Source CAD governs. Regenerate after design changes.

Documentation only; no electrical symbols

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Sheet: /Board guide and engineering reference/Routing guide/
File: Routing.kicad_sch

Title: AmiCube PiStorm68K / Routing guide

Size: A4	Date: 2026-10-16	**Rev: 1.8**
KiCad E.D.A. 10.0.7		Id: 15/16

Power and signal identification

Shared functional colour families for schematic labels and PCB copper.

GROUND	2		ADDRESS	26
POWER 5V	8		LATCH CONTROL	21
POWER 3V3	3		CPU CONTROL	46
POWER OTHER	3		JTAG	4
POWER CONTROL	32		INDICATORS	22
CLOCK	10		EXPANSION	2
DATA	32			

Follow each signal through the hierarchy by its global net label. Bus aliases summarize cross-sheet signal groups; the HTML guide lists every alias and colour assignment.

PCB Editor: Appearance → Objects → Net colours → All. Colour-only VIS_ classes preserve the original track, via and clearance rules.

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1.8 • generated 2026-10-16 • source 82030dcfa626

Units: mm. Source CAD governs. Regenerate after design changes.

Documentation only; no electrical symbols

Minimig.ca / Minimig

Sheet: /Board guide and engineering reference/Power and signal identification/
File: PowerAndBuses.kicad_sch

Title: AmiCube PiStorm68K / Power and signal identification

Size: A4	Date: 2026-10-16	Rev: 1.8
KiCad E.D.A. 10.0.7		Id: 16/16